

Prof. Veljko Milutinovic received his PhD from the University of Belgrade (rated #366 world-wide at the U.S. News Report 2015), spent about a decade on various faculty positions in the USA (mostly at Purdue University), and was a code signer of the DARPA's first GaAs RISC microprocessor. Now he teaches and conducts research at the University of Belgrade, in EE and MATH (Belgrade math is rated #87 world-wide at the U.S. News Report 2015). His research is mostly in data mining algorithms and dataflow computing, with the stress on mappings of algorithms onto architectures. His co-authored paper on matrix multiplication for dataflow received "The IET Premium Award for 2014" (meaning the best paper in IET Computing for 2012 and 2013). His paper on the research methodology was published in an ASCE journal and received a special recognition for the most downloaded paper of the publisher. He published over 20 books with the USA publishers. For 7 of his books, forewords were written by 7 different Nobel Laureates with whom he cooperated on his past industry sponsored projects. He has over 60 IEEE or ACM journal papers and about 3000 Google Scholar citations.